



9/11/02 And PAUL PATENT P

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	:	Songjie Xu) Group Art Unit 2123
Appl. No.	:	09/754,406 January 2, 2001) I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first-class mail in an envelope addressed to: United States Patent and Trademark Office, P.O. Box 2327, Arlington, VA 22202, on
For	:	METHODOLOGY AND APPLICATIONS OF TIMING- DRIVEN LOGIC RESYNTHESIS FOR VLSI CIRCUITS (as amended)) June 21, 2002 (Date)) Raimond J. Salenieks, Reg. No. 37,924
Examiner	:	Unknown)

PRELIMINARY AMENDMENT

United States Patent and Trademark Office P.O. Box 2327 Arlington, VA 22202

Dear Sir:

In response to the Notice of Omitted Item(s) in a Nonprovisional Application dated February 20, 2001 in the above-referenced patent application, please make the following amendments:

IN THE SPECIFICATION:

Please replace the original specification with the substitute specification attached hereto.

06/28/2002 SHINASS1 00000007 09754406

01 FC:203 02 FC:202 99.00 OP 126.00 OP